

Product Data (SMO-N)

TFG-787RH3, 786RH, 786XH

Features

- CMOS output
- Small size : 7W × 5D × 2Hmm
- One chip PLL with VCO

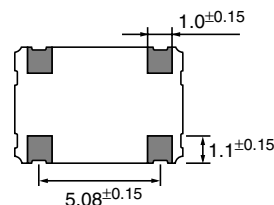
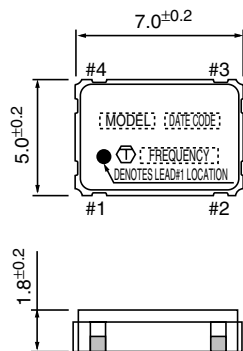


Specifications

Type		TFG-787RH3		TFG-786RH	TFG-786XH
Frequency	fo	70 to 140 MHz		70 to 112 MHz	
Frequency stability*	Δf/fo	±100 ppm		±50 ppm	
Operating temperature	Topr	0 to +70°C			
Supply voltage	Vcc	+5 VDC±10 %	+3.3 VDC±10 %	+5 VDC±10 %	+3.3 VDC±10 %
Supply current	Icc	60 mA Max. (70 ≤ fo ≤ 96 MHz) 70 mA Max. (96 < fo ≤ 120 MHz) 60 mA Max. (120 < fo ≤ 140 MHz)	40 mA Max. (70 ≤ fo ≤ 96 MHz) 50 mA Max. (96 < fo ≤ 140 MHz)	60 mA Max. (70 ≤ fo ≤ 96 MHz) 70 mA Max. (96 < fo ≤ 112 MHz)	40 mA Max. (70 ≤ fo ≤ 96 MHz) 50 mA Max. (96 < fo ≤ 112 MHz)
Input voltage	V _{IH} V _{IL}	70% Vcc Min. 20% Vcc Max.			
Output voltage	V _{OH} V _{OL}	90% Vcc Min. 10% Vcc Max.			
Symmetry	SYM	45 to 55% (50% Vcc level)			
Rise/Fall time	tr/tf	3 nSec. Max. (70 ≤ fo ≤ 140 MHz) at 20 to 80% Vcc		3 nSec. Max. (70 ≤ fo ≤ 112 MHz) at 20 to 80% Vcc	
Load capacitance	CL	30 pF Max. (70 ≤ fo ≤ 120 MHz) 15 pF Max. (120 < fo ≤ 140 MHz)		30 pF Max. (70 ≤ fo ≤ 112 MHz)	
Start-up time	t _{st}	2 mSec. Max.			
Measuring circuit		TEST- 5			
Sealing		Glass sealed			

* Inclusive of calibration tolerance at +25°C, operating temperature, operating voltage range.

Outline Drawing [mm]



Pin connections

1	E/D control
2	GND
3	Output
4	Vcc (DC)